

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| L1 | 7392 | ((level adj1 shift\$3) or (level adj1 conver\$4) or (level adj1 translat\$3)) and (current adj source) and (bias or reference) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 09:16 |
| L2 | 2937 | 1 and transistor and inverter | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 09:16 |
| L3 | 999 | 2 and ((bias adj1 voltage) or (reference adj1 voltage)) and (current adj1 mirror) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:44 |
| L4 | 460 | 3 and (high NEAR1 voltage) and ((low NEAR1 voltage) or (different NEAR1 voltage)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:21 |
| L5 | 215 | 4 and (constant adj1 current adj1 source) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:44 |
| L6 | 83 | 5 and ((down adj1 conver\$4) or (down adj1 shift\$3) or (down adj1 translat\$4) or (reduce\$2 adj1 voltage)) | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:46 |

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|-------|------|--|----------|------------------|---------|------------------|
| L1 | 2 | (((level adj1 shift\$3) or (level adj2 convert\$4) or (level adj2 translat\$4)) and ((first adj1 supply) or (high adj1 voltage)) and ((second adj2 supply) or (internal adj1 voltage) or (reduce\$2 adj1 voltage) or (low adj1 voltage)) and (ground or vee or vss) and (current adj1 source) and ((bias adj1 voltage) or (bias adj1 signal) or (reference adj1 voltage) or (reference adj1 signal)) and transistor\$1).clm. | US-PGPUB | OR | OFF | 2005/10/18 15:04 |